



JPW/B23

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Docket No.: CYPR-CD00185

I hereby certify that this transmittal of the below described document is being deposited with the United States Postal Service in an envelope bearing First Class Postage and addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on the below date of deposit.

Date of Deposit:	05/08/06	Name of Person Making the Deposit:	Mina Oliveri	Signature of the Person Making the Deposit:	<i>Mina Oliveri</i>
------------------	----------	------------------------------------	--------------	---	---------------------

Inventor(s): Warren Snyder, Craig Nemecek and Bert Sullam

Serial No.: 09/975,030

Group Art Unit: 2123

Filed: 10/10/01

Examiner: Craig, Dwin M.

Confirmation No: 7611

Title: EMULATOR CHIP/BOARD ARCHITECTURE AND INTERFACE

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

TRANSMITTAL OF FORMAL DRAWINGS

In response to Drawing Informalities

attached please find:

☒ (a) the formal drawings for this application  
Number of 5  
Sheets

Each sheet of drawing indicates the identifying indicia suggested in § 1.84(c) on the reverse side of the drawing

☐ (b) a copy of the NOTICE OF INFORMAL DRAWINGS

Please direct all correspondence concerning the above-identified application to the following address:

**WAGNER, MURABITO & HAO LLP**  
Two North Market Street, Third Floor  
San Jose, California 95113  
(408) 938-9060

Respectfully submitted,

Date:

*May 8, 2006*

By:

*[Signature]*

Anthony C. Murabito  
Reg. No. 35,295